capacitor (C1) connected in parallel to the first resistor (R1).

11. (New) The video control circuit set forth in claim 9, wherein the sensing means (Q2, R5, C2) comprises a second capacitor (C2) connected to the second terminal of the second resistor (R2).

12. (New) The video control circuit set forth in claim 6, wherein the feedback circuit comprises an amplifier (Q3) having an input coupled to the second terminal of the second resistor (R5) and an output for transmitting the beam limiting signal (VABL) to the video processor (V1).

REMARKS

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Respectfully submitted,

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